

# **ABSTRACT**

Using the method of the present invention, clock mismatch or drift between a packet network transmitter and receiver can be compensated for via receiver clock rate change or bit rate change.

- 5 The method includes estimating the average receiver FIFO buffer memory level over time, changing the packet network receiver bit rate or clock rate according to the average receiver FIFO buffer memory level change while still satisfying the MPEG-2 system specification.